



Date: 04th December, 2025

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| To The General Manager Department of Corporate Services BSE Ltd, P. J. Towers, Dalal Street, Mumbai – 400 001 Scrip code: 532407 | To The Listing Department National Stock Exchange of India Limited Exchange Plaza, Bandra Kurla Complex, Mumbai – 400 051 Scrip Symbol: MOSCHIP |
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Dear Sir/Madam,

Sub: Submission of a copy of press release on “MosChip Collaborates with EMASS on Silicon Implementation for its Breakthrough Edge AI SoC”.

Ref: Disclosure under Regulation 30 of the Securities and Exchange Board of India (Listing Obligations and Disclosure Requirements) Regulations 2015.

Pursuant to Regulation 30 of the Securities and Exchange Board of India (Listing Obligations and Disclosure Requirements) Regulations 2015, we enclose herewith a press release on “**MosChip Collaborates with EMASS on Silicon Implementation for its Breakthrough Edge AI SoC**”.

A copy of the press release would also be placed on the website of the Company at www.moschip.com

Kindly take the above information on your records.

Thanking you,

Yours faithfully,

For MosChip Technologies Limited,

CS Suresh Bachalakura
Company Secretary

MosChip Technologies Limited

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MosChip Collaborates with EMASS on Silicon Implementation for its Breakthrough Edge AI SoC

MosChip's silicon and product engineering execution supports EMASS's ultra-low-power ECS-DoT SoC

Santa Clara, USA – Dec 4, 2025 – MosChip Technologies, a leading player in [silicon and product engineering services](#), today announced its role in supporting EMASS's ECS-DoT Edge AI System-on-Chip (SoC), an ultra-low-power chip designed for always-on intelligence in wearables, drones, industrial IoT, and edge sensors.



EMASS led the ECS-DoT's architecture design and set ambitious targets for performance and energy efficiency, aiming for up to 93% faster processing and 90% lower energy use compared to conventional edge AI solutions. To support this effort, MosChip provided [engineering services for the silicon](#) implementation in 22nm technology - contributing to physical design flows, tape-out coordination, packaging, assembly, evaluation hardware, and validation activities. The collaboration enabled a fully functional SoC and evaluation platform.

"This collaboration demonstrates the impact of pairing innovative SoC architecture with experienced silicon engineering support," said Srinivasa Rao Kakumanu, CEO & Managing Director at MosChip Technologies. "EMASS defined a highly differentiated architecture, and MosChip was pleased to support its silicon implementation through our physical design and [product engineering services](#)."

ECS-DoT: Compact AI at the Extreme Edge

The ECS-DoT is a RISC-V-based Edge AI SoC that integrates dual neural accelerators and 4MB of on-chip memory. It enables real-time, milliwatt-class inference for vision, audio, and sensor workloads without cloud dependency. Optimized for power- and space-constrained applications,

ECS-DoT provides always-on AI capabilities for drones, wearables, healthcare trackers, and industrial monitoring systems.

“MosChip played an important role in supporting the silicon implementation of our architecture,” said Mark Goranson, CEO at EMASS. “Their contributions in physical design, product engineering, and validation complemented our internal efforts and helped us bring a robust SoC to our OEM partners.”

About EMASS

EMASS – a subsidiary of Nanoveu Ltd (ASX: NVU) – is an advanced semiconductor company specializing in ultra-low-power AI system-on-chip (SoC) solutions for edge computing. The company's flagship ECS-DoT chip delivers high-performance AI processing for vision, audio, and sensor data directly on-device, maximizing energy efficiency through its RISC-V architecture and non-volatile memory technologies. This always-on intelligence solution is optimized for power- and space-constrained applications including drones, wearables, healthcare devices and industrial IoT systems. For more information, visit nanoveu.com/emass.

About MosChip

MosChip Technologies is a leading silicon and product engineering company with 26 years of expertise spanning silicon design, embedded systems, digital engineering, and AI. From ASICs to AI-led products, MosChip partners with global semiconductor companies and OEMs to design, engineer, and deliver next-generation solutions across industries. For more information, visit: www.moschip.com

SAFE HARBOR: This release comprises certain forward-looking statements that involve risks and uncertainties. Our actual results could differ materially from those mentioned in such forward-looking statements.

The risks and uncertainties include but are not limited to, those risks and uncertainties, viz, our ability to compete in a highly competitive semiconductor industry, ability to define, develop, and sell new products, dependency on subcontractors for the supply and quality of raw material, dependency on markets considering the cyclical nature of the industry and our ability to attract and retain technical manpower. MosChip may from time to time make additional forward-looking statements in any manner and does not undertake to update any of these forward-looking statements that may be made from time to time by or on behalf of the company.